



# Project Collaboration

This page is for project document exchange and coordination.

## Document Archive: DBBC

Authors	Date	Document	Comments
Gino Tuccari	14.11.2007	<a href="#">Core1pinout.xls</a>	List of pins of HSI, HSIR, HSO, HSOR, CCCM, CCCMR bus /connectors and connections with the FPGA pins
Dave Graham	04.10.2007	<a href="#">DBBCTest-2007oct04.pdf</a>	Successful EURO89 dBBC Test
Dave Graham	03.01.2008	<a href="#">DBBCTest-2008jan03.pdf</a>	Quick look at dBBC in EURO90
Tuccari, Buttaccio, Nicotra, Ziang, Wunderlich	Jan 2006	<a href="#">gm2006_4-01_tuccari.ppt</a>	DBBC - A Flexible Platform for VLBI Data Process (4th IVS General Meeting, Presentation, 2006
Tuccari, Buttaccio, Nicotra, Ziang & Wunderlich	Jan 2006	<a href="#">tuccari1.pdf</a>	DBBC - A Flexible Platform for VLBI Data Process (4th IVS General Meeting, Proceedings, 2006)

Wunderlich et al.	06.12.2007	<a href="#">DBBCposter-2007dec06.pdf</a>	DBBC poster
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## Notes

Author	Date	Note
Alan Roy	09.05.2008	Virtex 5 device on Core2 board has part number XC5VLX220-1FFG1760CES
Alan Roy	09.05.2008	Virtex II device on Core board has part number XC2V3000

## DBBC Meeting Minutes

Date	Topics	Minutes
12.03.2008	Fila10G demo board, firmware issues for UDP protocol, manpower, project planning. Prospects for a spin-off company through INAF	<a href="#">MinutesDBBC-080312.txt</a>

## Document Archive: L-Band Digital Receiver

Authors	Date	Document	Comments
Gino Tuccari	Nov 2006	<a href="#">LBANDDIGITALRECEIVER.doc</a>	Project proposal and outline of a digital receiver for L band at Effelsberg
Tuccari, Keller, Winkelmann, Nalbach	30.11.2006	<a href="#">StatusReport_DiRec_20061130.pdf</a>	Status report
Michael Nalbach	27.09.2006	<a href="#">Digital_Rec_Analogue_Block_Diagr.pdf</a>	Block schematic of the analogue signal conditioner (gain and power level control)
Reinhard Keller	17.01.2007	<a href="#">DigitalReceiver18-21cm.ppt</a>	(in german) Block schematics and CAD image of the digital receiver

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## Links

<b>General</b>	
<a href="http://www.noto.ira.inaf.it/index.php/project">http://www.noto.ira.inaf.it/index.php/project</a>	DBBC project web page
<b>VSI-E</b>	
<a href="http://www.haystack.mit.edu/tech/vlbi/vsi/index.html">http://www.haystack.mit.edu/tech/vlbi/vsi/index.html</a>	VSI-E specification for eVLBI data transport over IP
<a href="http://www.haystack.mit.edu/tech/vlbi/evlbi/presentations/VSI_Tutorial.ppt">http://www.haystack.mit.edu/tech/vlbi/evlbi/presentations/VSI_Tutorial.ppt</a>	VSI-E tutorial, Ruszczyk 2006
<a href="http://www.jive.nl/dokuwiki/doku.php/expres:evlbi_meetings_historic">http://www.jive.nl/dokuwiki/doku.php/expres:evlbi_meetings_historic</a>	Links to all eVLBI Workshop pages